

## Problem \#8 (15 minutes)

You are to realize the truth table given below .

The chips are drawn below. You are to label the chips (e.g., with the TTL part number and pin numbers). Then you are to "wire" the circuit to realize Z. You may assume that uncomplemented $\mathrm{a}, \mathrm{b}, \mathrm{c}$ and dinputs are available. On the last few pages of this quiz are data sheets for some TTL chips you may find useful.


## Extra Credit: (5 minutes)

List three practical ideas for improving the lab. The ideas should not entail additional resources. For example, having a lab TA available for consultation 24 hours per day, 7 days per week might be nice, but it is not practical.

